

FIG. 1

FIG. 2 is a block diagram of a system 200. The system 200 includes a processor 201, a processor bus 210, a bridge/memory controller 211, memory 213, a bus 220, a network controller 221, a display device controller 222, a video camera 223, a bus bridge 224, a data storage 231, a keyboard interface 232, and an audio controller 233. The processor 201 is connected to the processor bus 210. The processor bus 210 is connected to the bridge/memory controller 211. The bridge/memory controller 211 is connected to the memory 213. The bridge/memory controller 211 is connected to the bus 220. The bus 220 is connected to the network controller 221, the display device controller 222, the video camera 223, and the bus bridge 224. The bus bridge 224 is connected to a second bus 230. The second bus 230 is connected to the data storage 231, the keyboard interface 232, and the audio controller 233.

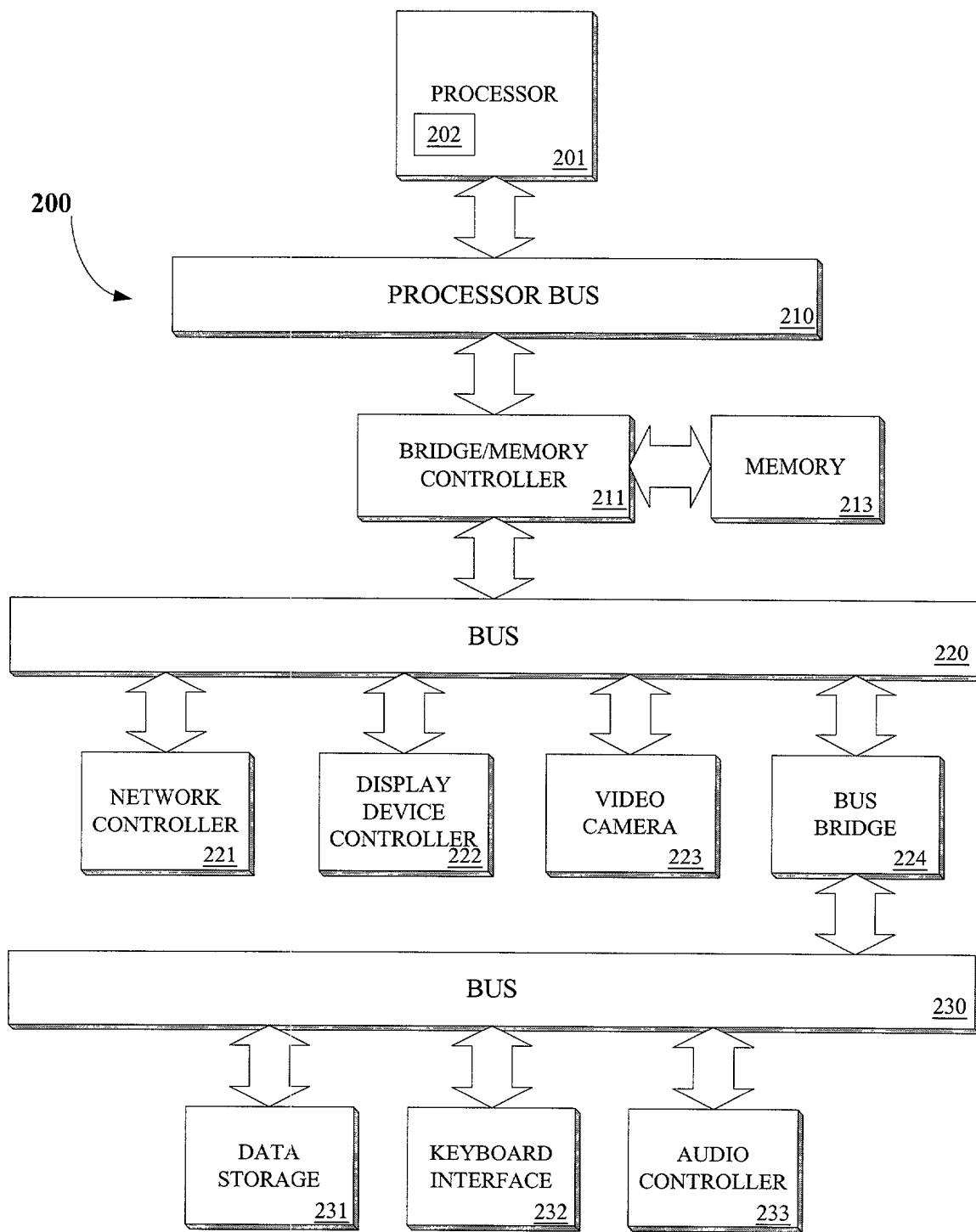


FIG. 2

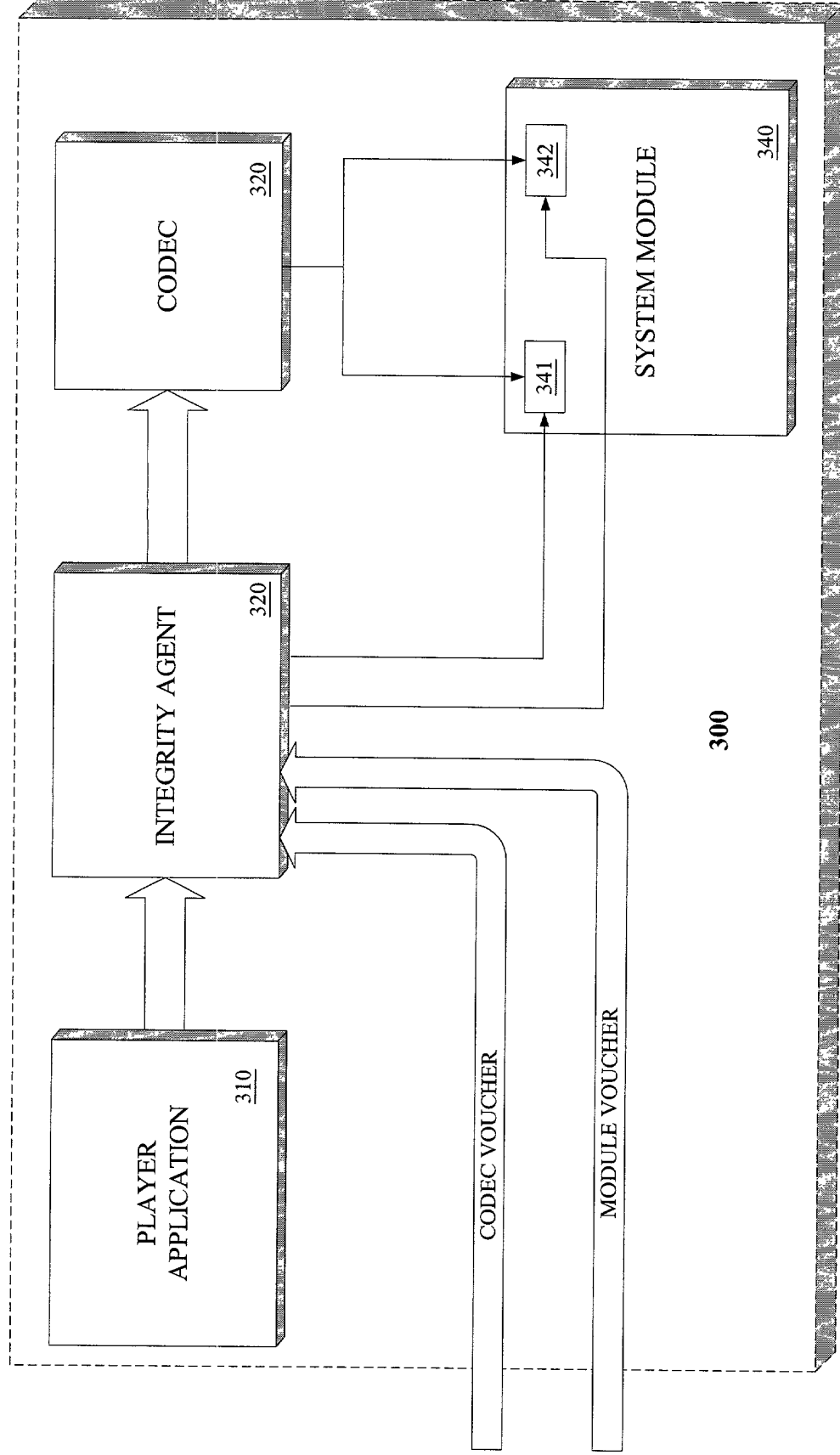


FIG. 3

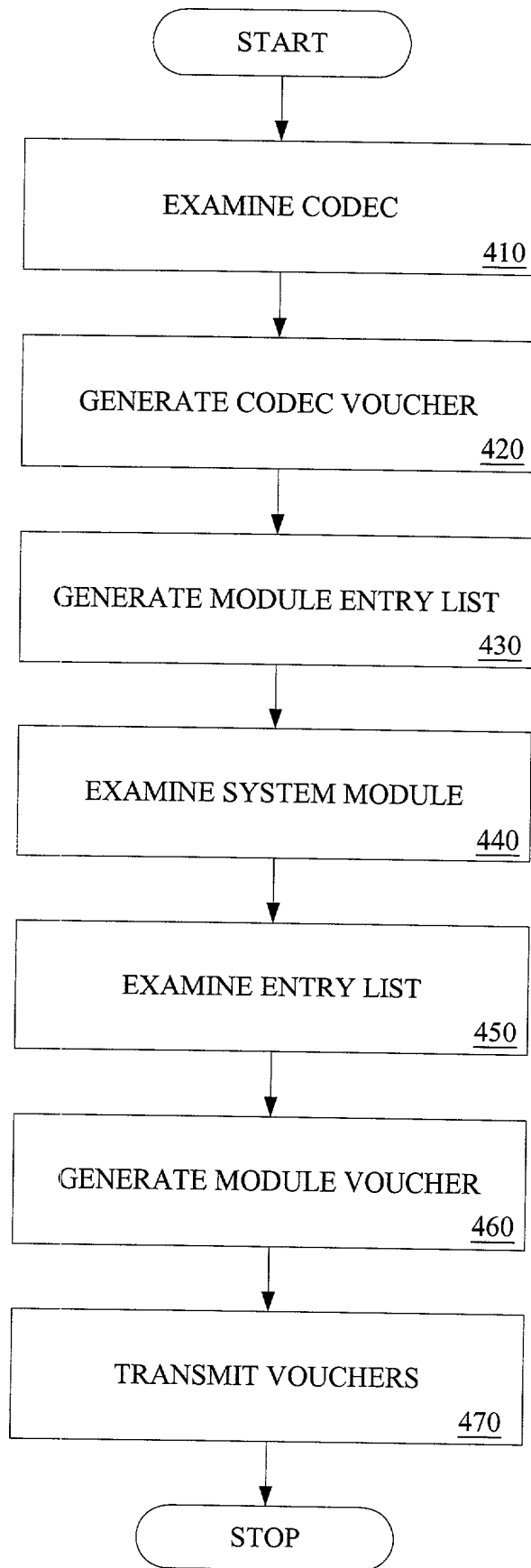


FIG. 4

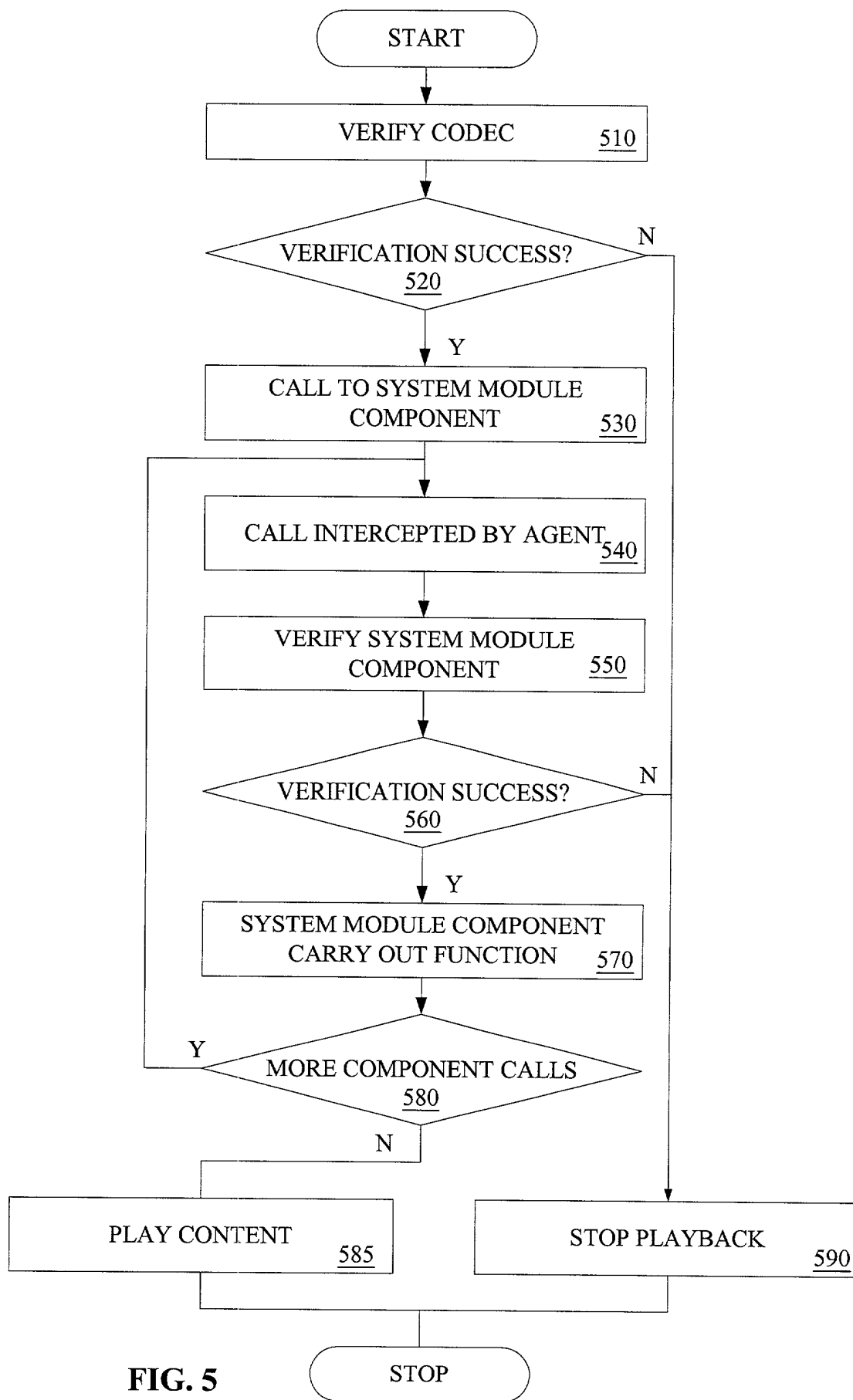


FIG. 5